TSC	TS5204 150mA Low Noise LDO Voltage Regulator				
SOT-23	SOT-89 1 2 3	Pin assignment SOT-89 1. Output 2. Ground 3. Input SOT-23 1. Output 2. Input 3. Ground	Low Power Consumption Low Dropout Voltage 0.275V		
General E	Description				

The TS5204 series is an efficient linear voltage regulator with ultra low noise output, very low dropout voltage (typically 17mV at light loads and 165mV at 150mA), and very low power consumption (600uA at 100mA), providing high output current even when the application requires very low dropout voltage. The TS5204 series is included a precision voltage reference, error correction circuit, a current limited output driver, over temperature shutdown and revered battery protection.

The TS5204 series is available in 3-pin SOT-23 and SOT-89 package.

Features **Ordering Information** ∻ Ultra low noise output. Part No. **Operating Temp.** Package -40 ~ +125 °C ∻ TS5204CX**xx** SOT-23 Output current up to 150mA TS5204CY<u>xx</u> SOT-89 ∻ Low dropout voltage Note: Where xx denotes voltage option, available are ∻ Low power consumption 5.0V, 3.3V, 3.0V, 2.9V, 2.8V, 2.5V and 1.8V. Leave ∻ Internal current limit blank for adjustable version. Contact factory for additional voltage options. ∻ Thermal shutdown protection Applications Typical Application Circuit ∻ Cellular telephones Input Output ∻ Out Palmtops, notebook computers In σ Gnd ∻ Battery powered equipment ∻ Consumer and personal electronics C1 C2 5V ∻ SMPS post regulator and DC to DC modules 1uF 2.2uF ∻ High-efficiency linear power supplies

Absolute Maximum Rating (Note 1)

Input Supply Voltage		Vin	-20~ +20	V
Power Dissipation (Note 2)		P _D	Internal limited	
Thermal Resistance	SOT-23	Ѳја	220	°C/W
	SOT-89		180	
Operating Junction Temperature Range		Tj	-40 ~ +125	°C
Storage Temperature Range		T _{STG}	-65 ~ +150	°C
Lead Soldering Temperature (260 °C)			5	S



Input Supply Voltage	Vin	+2.5 ~ +16			V	
Electrical Characteristic	S					
Vin=Vo+1V, Io=100uA, Cout=1uF,	Vce≥2V, Tj =	25 °C, unless other	wise specified.			_
Parameter	Co	nditions	Min	Тур	Max	Unit
Output Voltage	Vin=Vo + 1V	,	0.99 Vo	Vout	1.01 Vo	V
	Vin=Vo + 1V	′, Io= 50mA	0.98 Vo		1.02 Vo	
Output Voltage Temp. Coefficient	(Note 4)			40		ppm/ °C
Line Regulation	$Vo+1V \le Vin$	≤ 16V		0.005	0.05	%/V
Load Regulation (Note 5)	$0.1\text{mA} \le \text{lo} \le 150\text{mA}$			0.02	0.2	%/V
Dropout Voltage (Note 6)	lo=100uA			17	50	
	lo=50mA			110	150	mV
	lo=100mA			140	250	
	lo=150mA			165	275	
Ground Pin Current (Note 7)	lo=100uA			80	125	
	lo=50mA			350	600	uA
	lo=100mA			600	1000	
	lo=150mA			1300	1900	
Output Current Limit	Vout=0V			300	500	mA
Power Supply Rejection Ratio	At f=100Hz, lo=100uA,			75		dB
Thermal Regulation (Note 8)				0.05		%/W
Output Noise	lo=50mA, Co	out=2.2uF,		260		nV√Hz
	470pF from	bypass to Ground				

Note 1: Exceeding the absolute maximum rating may damage the device.

- Note 2: The maximum allowable power dissipation at any Ta is Pd(max) = [Tj(max) Ta] + Θja. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- Note 3: The device is not guaranteed to function outside its operating rating.
- Note 4: Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- Note 5: Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 1mA to 150mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- Note 6: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.
- Note 7: Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.
- Note 8: Thermal regulation is defined as the change in output voltage at a time "t" after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 150mA load pulse at Vin=16V for t=10mS.



Application Information

Thermal Characteristics

TS5204 series is designed to provide 150mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-ambient thermal resistance of the device and the following basic equation:

Pd(max) = [Tj(max) – Ta] /Øja

Tj(max) is the maximum junction temperature of the die(125°C), and Ta is the ambient operating temperature. Oja is layout dependent, the actual power dissipation of the regulator circuit can be determined using the equation:

Pd = (Vin – Vout) * lout + Vin * Ignd

Substituting Pd(max) for Pd and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. For example, when operating the TS5204CX33 at room temperature with a minimum footprint layout, the maximum input voltage for a set output current can be determined as follows:

Pd(max) = (125°C – 25°C) / 220°C/W Pd(max) = 455mW

The junction to ambient thermal resistance for the minimum footprint is 220°C/W, the maximum power dissipation must not be exceeded for proper operation. Using the output voltage of 3.3V and an output current of 150mA, the maximum input voltage can be determined. Formt eh electrical characteristics table, the maximum ground current for 150mA output current is 2.5mA.

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445mW = ( Vin - 3.3V ) * 150mA + Vin * 2.5mA
445mW = Vin * 150mA - 3.3 * 150mA + Vin * 2.5mA
445mW = Vin * 150mA - 495mW + Vin * 2.5mA
950mW = Vin * 152.5mA
Vin(max) = 6.23v
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Therefore, a 3.3V application at 150mA of output current can accept a maximum input voltage of 6.23V in a SOT-23 package.

Input Capacitor Requirement

An input capacitor of 1uF or greater is recommended when the device is more than 10" away from the bulk AC supply capacitance or when the supply is a battery.

Output Capacitor Requirement

The TS5204 series requires an output capacitor to maintain stability and improve transient response is necessary. 2.2uF minimum is recommended. Larger values improve the regulator's transient response. The output capacitor value may be increased without limit. The output capacitor should have an ESR (effective series resistance) less than 5Ω and a resonant frequency above 1MHz. Ultra low ESR capacitors can cause a low amplitude oscillation on the output and/or under damped transient response. Most of tantalum or aluminum electrolytic capacitors are adequate; film types will work. Since many aluminum electrolytic have electrolytes that freeze at about –30°C, solid tantalums are recommended for operation below –25°C. At lower values of output current, less output capacitance is required for output stability. The capacitor can be reduced to 0.47uE for current below 10mA or 0.33uE for currents below 1mA.

No Load Stability

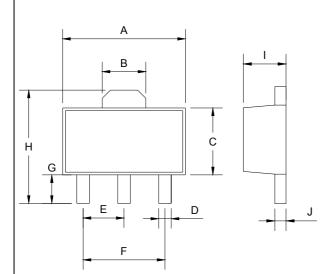
The TS5204 series iwill remain stable and in regulation with no load, unlike many other voltage regulators. This is especially important in CMOS RAM keep alive applications.

Dual Supply Operation

When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage must be diode clamped to ground.

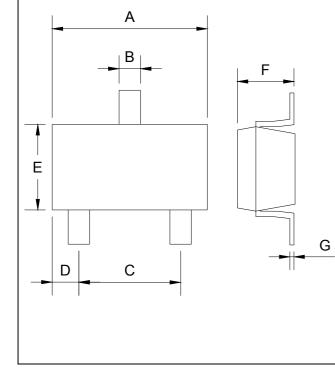


SOT-89 Mechanical Drawing



	SOT-89 DIMENSION					
DIM	MILLIM	ETERS	INCHES			
DIN	MIN	MAX	MIN	MAX		
А	4.40	4.60	0.173	0.181		
В	1.50	1.7	0.059	0.070		
С	2.30	2.60	0.090	0.102		
D	0.40	0.52	0.016	0.020		
Е	1.50	1.50	0.059	0.059		
F	3.00	3.00	0.118	0.118		
G	0.89	1.20	0.035	0.047		
Н	4.05	4.25	0.159	0.167		
I	1.4	1.6	0.055	0.068		
J	0.35	0.44	0.014	0.017		

SOT-23 Mechanical Drawing



	SOT-23 DIMENSION					
	MILLIMETERS		INCHES			
DIM	MIN	MAX	MIN	MAX		
А	2.88	2.91	0.113	0.115		
В	0.39	0.42	0.015	0.017		
С	1.78	2.03	0.070	0.080		
D	0.51	0.61	0.020	0.024		
Е	1.59	1.66	0.063	0.065		
F	1.04	1.08	0.041	0.043		
G	0.07	0.09	0.003	0.004		